

FORM PTO-449

INFORMATION DISCLOSURE CITATION

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Applicant

Gary S. DITLOW, et al.

Filing Date

Group Art Unit

Unassigned

JC932 U.S. PTO 09/677913

10/03/00

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
AB	AA	5,404,310	04/04/95	Mitsuhashi	364	490	
	AB	5,537,328	07/16/96	Ito	364	489	
	AC	5,598,348	01/28/97	Rusu et al.	364	491	
	AD	5,838,947	11/17/98	Sarin	395	500	
	AE	5,872,952	02/16/99	Tuan et al.	395	500	
	AF	5,883,814	03/16/99	Luk et al.	364	491	
	AG	5,903,469	05/11/99	Ho	364	489	
✓	AH	5,933,358	08/03/99	Koh et al.	364	578	
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub-Class	Translation
	AL						Yes No
	AM						Yes No
	AN						Yes No
	AO						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	✓	Farbarik, R. Xiaowen Liu, Rossman, M. Parakh, P. Basso, T. Brown, R.; <i>Cad Tools For Area Distributed I/O Pad Packaging</i> , Proceedings, 1997 IEEE Multi-Chip Module Conference (Cat. No.97CB36039) 1997 p.125-9.
AB	AS	✓	Richard Goering; <i>Analysis Tool Touts Integration</i> , Electronic Engineering Times, February 9, 1999, p. PG45.
AB	AT	✓	Richard Goering; <i>Cadence Adds 'Integrity' Features to IC Layout</i> ; Electronic Engineering Times, November 8, 1999, p. PG63.

Examiner

ABrooks

Date Considered

14 April 2004

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609.

Draw line through citation if not in conformance and not considered.

Include copy of this form with next communication to Applicant.